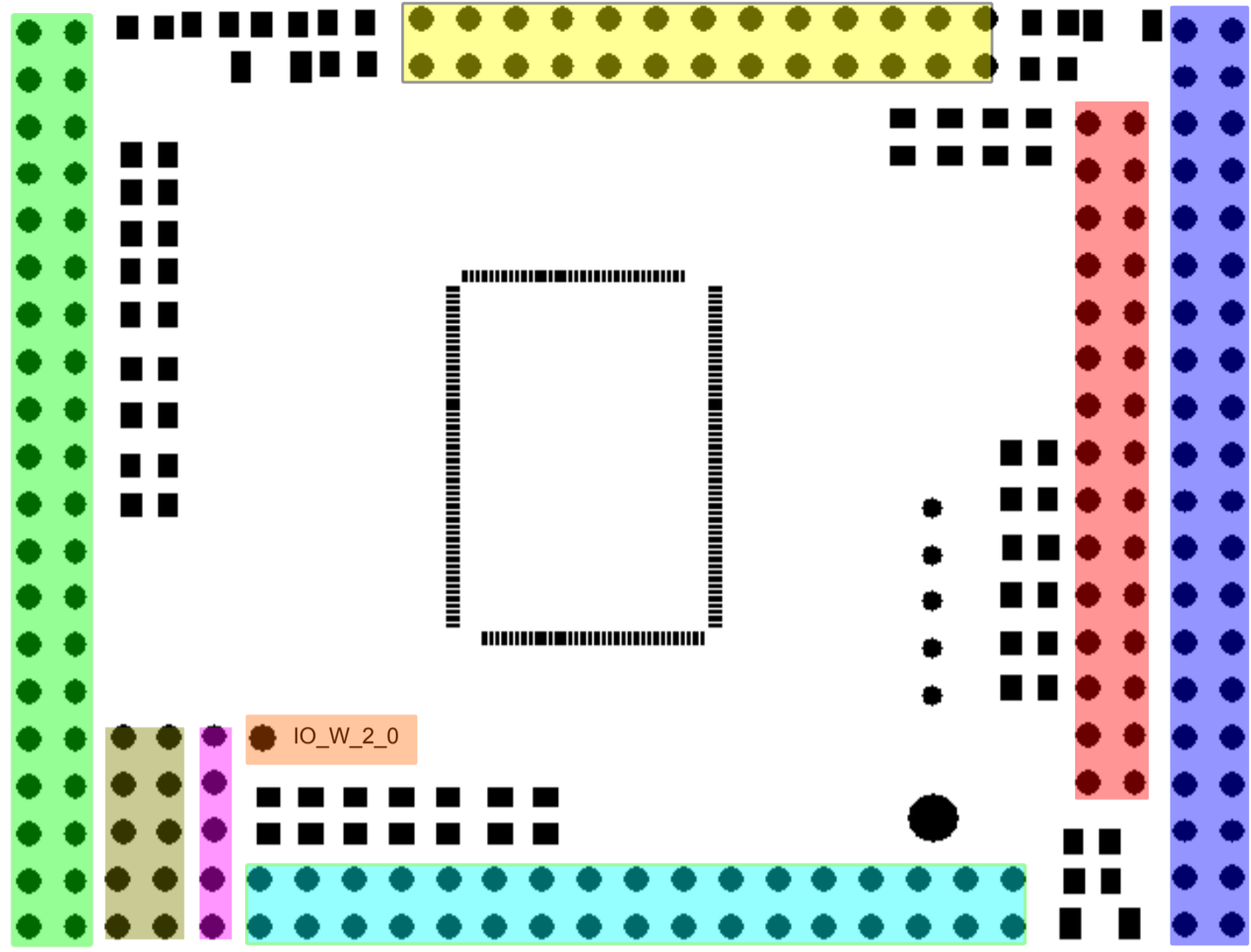


RASP30 IC Board_Header Labels

Rev 2: 25 May 2013

(Corresponding to PCB: rasp30_ic_board.brd)



- Drain ●● ICHAR
- Pulse ●● I2VOUT
- Gate ●● DBG_FREEZE
- GPI_14 ●● GPI_15
- GPI_12 ●● GPI_13
- REQ_IN_30N ●● ACK_IN_30N
- AER_IN_6_30N ●● AER_IN_7_30N
- AER_IN_4_30N ●● AER_IN_5_30N
- AER_IN_2_30N ●● AER_IN_3_30N
- AER_IN_0_30N ●● AER_IN_1_30N
- AER_Out_3_30N ●● AER_OUT_2_30N
- AER_Out_1_30N ●● AER_OUT_0_30N
- AER_ACK_Out_30N ●● AER_REQ_Out_30N
- DBG0_30N_IO_W_27 ●● DBG1_30N_IO_W_26
- DBG2_30N_IO_W_25 ●● DGB3_30N_IO_W_24
- DBG4_30N_IO_W_23 ●● DGB6_30N_IO_W_22
- IO_W_20 ●● IO_W_15
- IO_W_7_6 ●● IO_W_6_5
- IO_W_5_4 ●● IO_W_4_3
- IO_W_3_2 ●● IO_W_2_1

- Camera_SDA ●● Camera_EXTCLK
- Camera_VD ●● Camera_RESET
- Camera_HD ●● Camera_SCL
- GND ●● GND
- GND ●● GND
- Camera_DCLK ●● Camera_DCLK
- Vout_1_5 ●● Vout_1_5
- Vout_2_8 ●● Vout_2_8
- HVB_0 ●● HVB_0
- AER_IN_3H_0 ●● AER_IN_3H_0

- SDI_MASTER_1
- SPI_CLK_MASTER_1
- SPI_CLK_MASTER_0
- CS_N_MASTER_0
- SDO_MASTER_0
- SPI_ADDR_3
- SPI_ADDR_1
- 430_SPI_CLEAR_N
- SCAN_MODE
- TEST_Si2
- NMI
- TEST_SO2
- GND
- GND
- SDI_MASTER_0
- CS_N_MASTER_1
- SDO_MASTER_1
- SPI_ADDR_4
- SPI_ADDR_2
- USE_UC
- SPI_ADDR_0
- SCAN_ENABLE
- TEST_Si1
- TEST_Si3
- CPU_EN
- TEST_SO3

- AER_IN_3H_2
- S0_AIN_4_30H
- S2_AIN_6_30H
- S5_AER_ACK_IN_3H
- S21_CHARINP_3H
- S23_TIABUFIN_3H
- TIABUFOUT_3H
- HVBCHAR_3H
- GDBGN_3H
- SDDBG_3H
- THRDBG_3H
- VDD_CD__SGIDBG_3H
- S25_LVB_16_30H
- S27_LVB_18_30H
- VTUN_EXT
- AVDD_33
- AVDD_25
- AER_IN_3H_1
- AER_IN_3H_3
- S1_AIN_5_30H
- S3_AIN_7_30H
- S6_AER_REQ_IN_3H
- S22_TIA_CHARINN_3H
- TIA_CHAROUT_3H
- LVBCHAR_3H
- SGDBG_3H
- STDBG_3H
- VMDBG_3H
- S24_LVB_15_30H
- S26_LVB_17_30H
- VSS_CD
- ESD_VDD
- DVDD_33
- DVDD_25

- GND
- IVDD_SEL_DEPRECATED
- GPOUT_13
- GPOUT_15
- REQ_OUT_30H_BIAS_10_30N
- AOUT_6_30H_BIAS_12_30N
- AOUT_4_30H_BIAS_14_30N
- AOUT_2_30H_BIAS_16_30N
- AOUT_1_30H_BIAS_17_30N
- CHAR_BIAS_3N_E22
- TIA_CHAR_INP_3N_E20
- TIA_CHAR_OUT_3N_E18
- TIA_OUT_BUF_3N_E16
- IO_PAD_E_3
- IO_PAD_E_1
- GND
- DRESET
- GPOUT_12
- GPOUT_14
- ACK_OUT_30H_BIAS_9_30N
- AOUT_7_30H_BIAS_11_30N
- AOUT_5_30H_BIAS_13_30N
- AOUT_3_30H_BIAS_15_30N
- AOUT_0_30H_BIAS_18_30N
- TIA_VCLAMP_3N_E21
- TIA_CHAR_INN_3N_E19
- TIA_IN_BUF_3N_E17
- IO_PAD_E4
- IO_PAD_E2
- IO_PAD_E0
- IVDD

- ESD_VDD
- AVDD_33
- DVDD_33
- DVDD_25
- AVDD_25
- IVDD
- AVDD_6
- 6V_EN
- 12V_EN
- DSS1077_OUT1
- VTUN_EXT
- 430_SPI_SDI
- 430_SPI_CLK
- 430_SPI_CS_N
- 430_SPI_SDO
- RESET_N
- DBG_EN
- RXD_430
- DCO_CLK
- TXD_430
- DS1077_CTRL1
- DS1077_CTRL0
- SEL_AVDD_6
- SEL_VTUN_EXT
- SEL_IVDD_0
- SEL_IVDD_1
- DS_1077_SCL
- DS_1077_SDA
- SEL_DVDD_25
- SEL_ESD_VDD
- SEL_AVDD_25
- GND
- AVDD_33
- AVDD_6
- GND
- DVDD_33
- IVDD
- IVDD
- GND
- GND