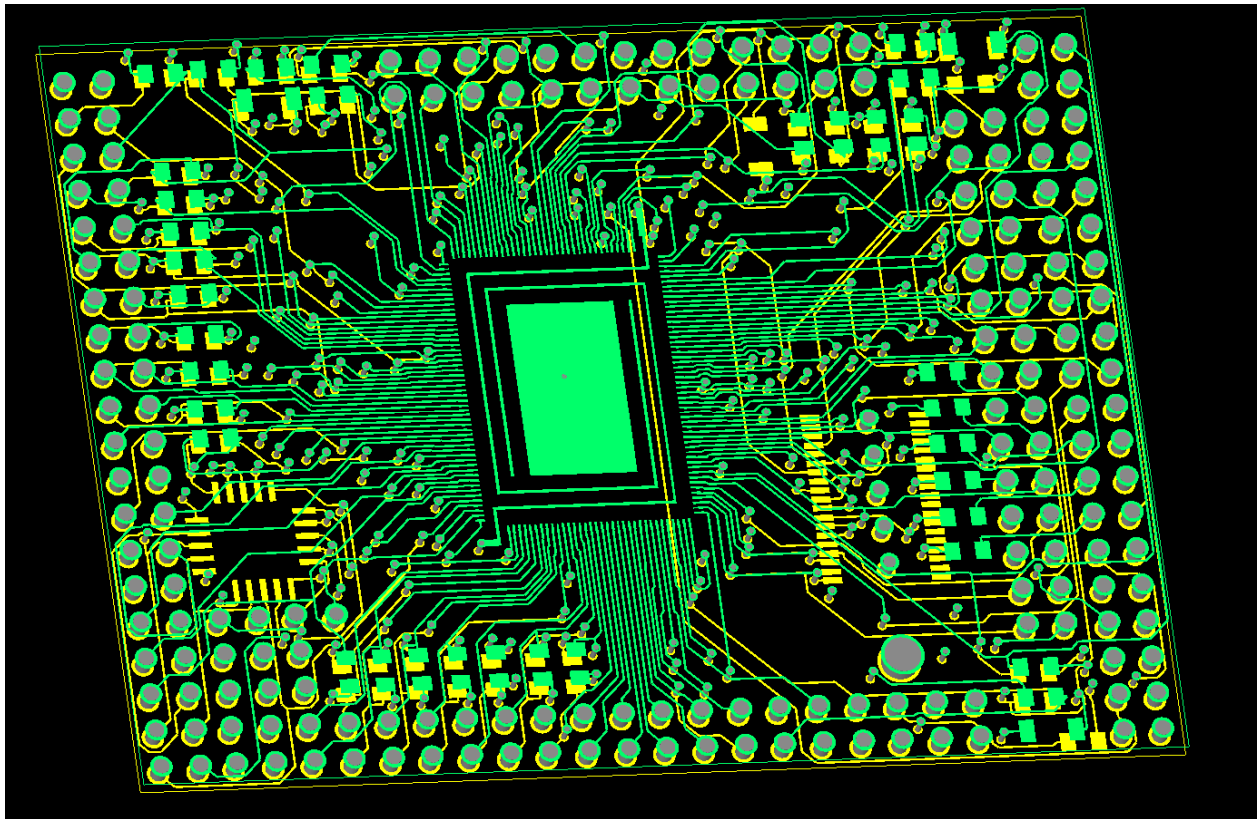


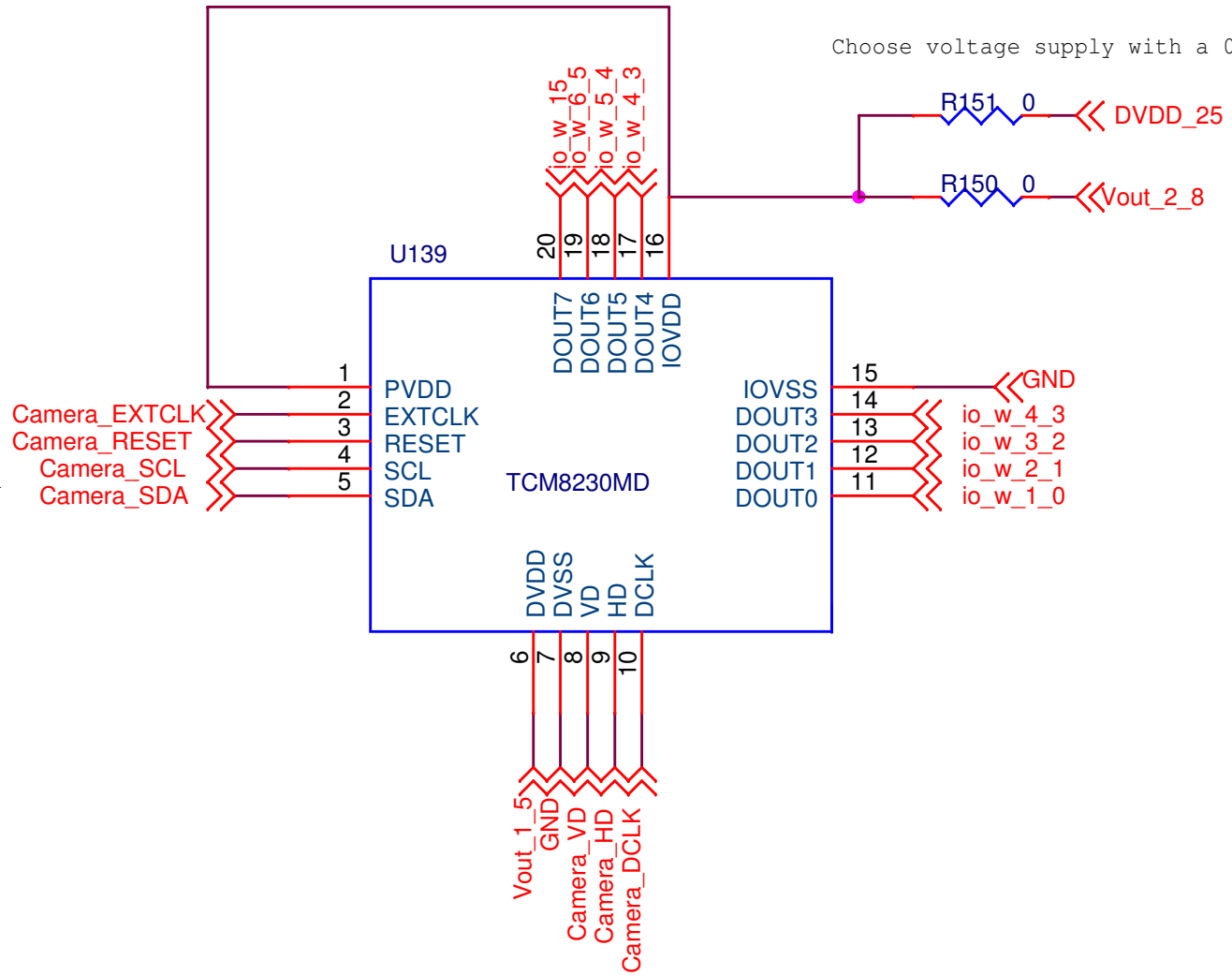
RASP30_IC_Board (Rev1)



Allegro Schematic:
rasp30_ic_board.opj

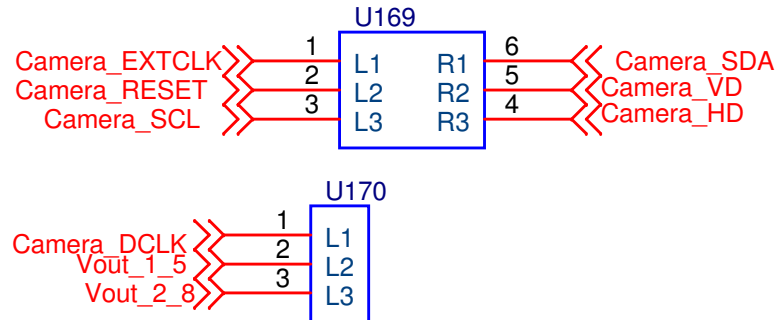
Allegro PCB:
rasp30_ic_board.brd

SCL = clock for I2C-bus command
 SDA = Data for I2C-bus command



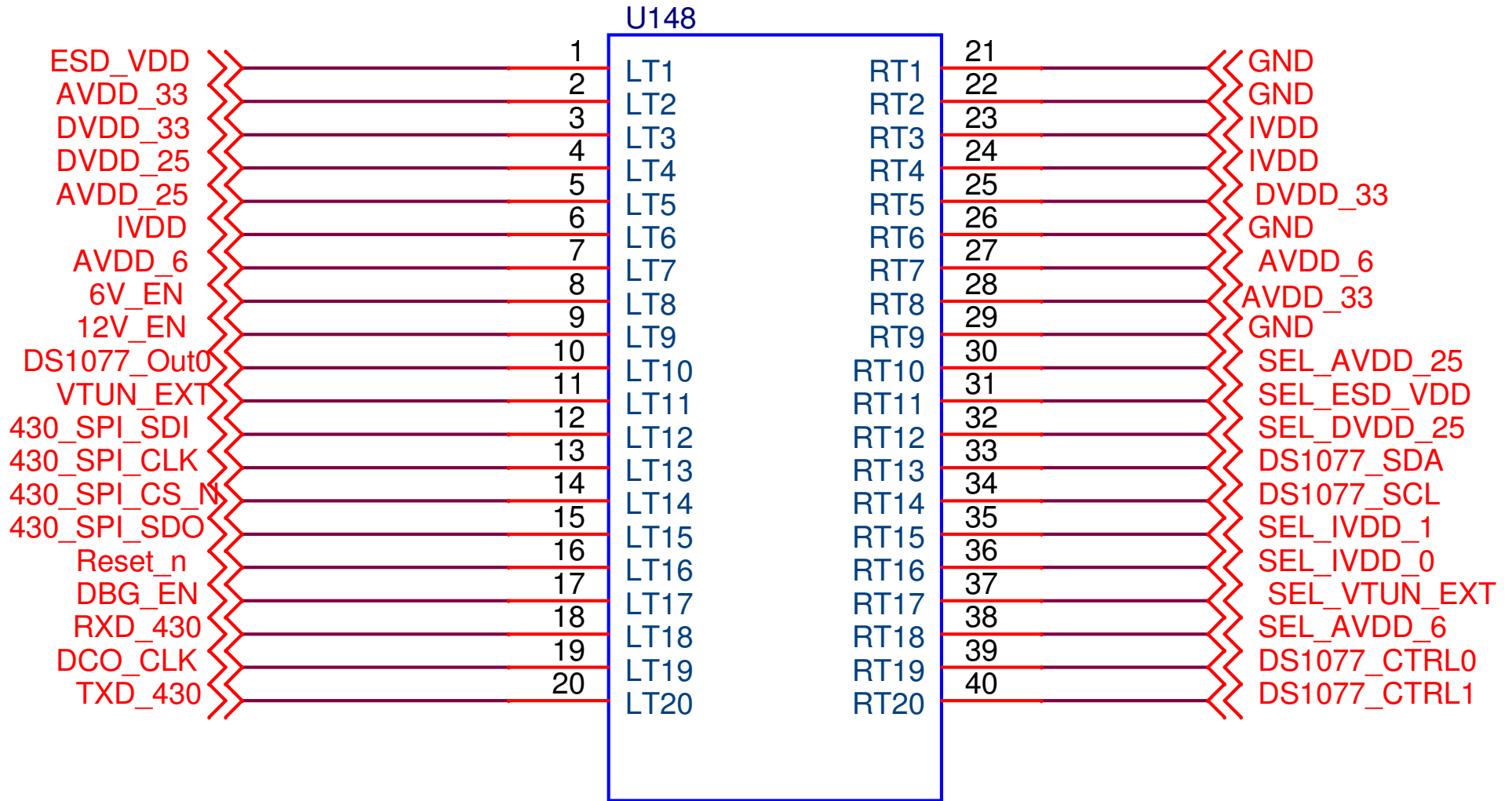
Clock for external INPUT

SCL & SDA for I2C



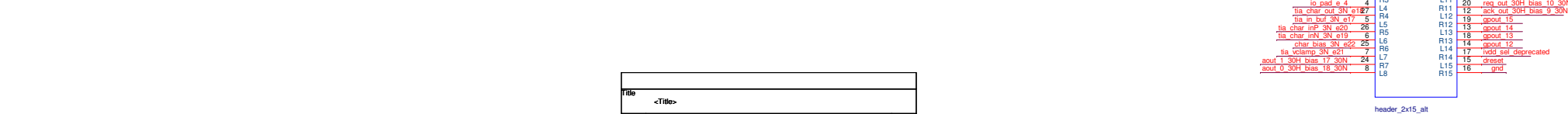
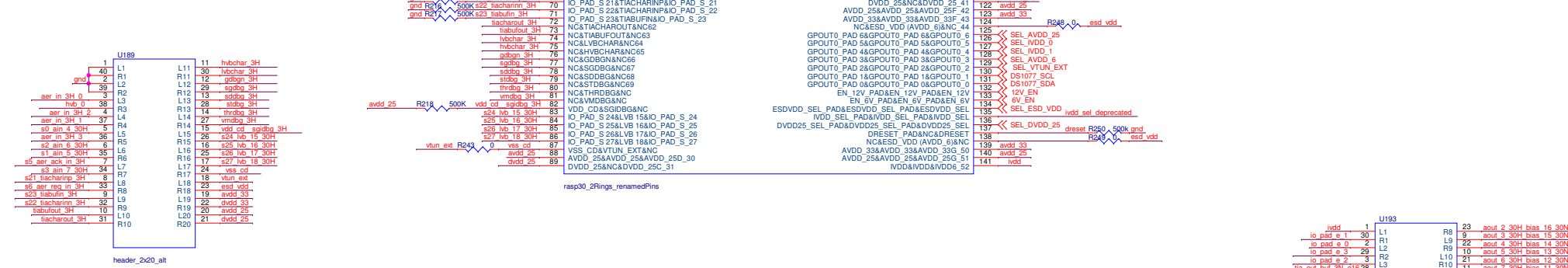
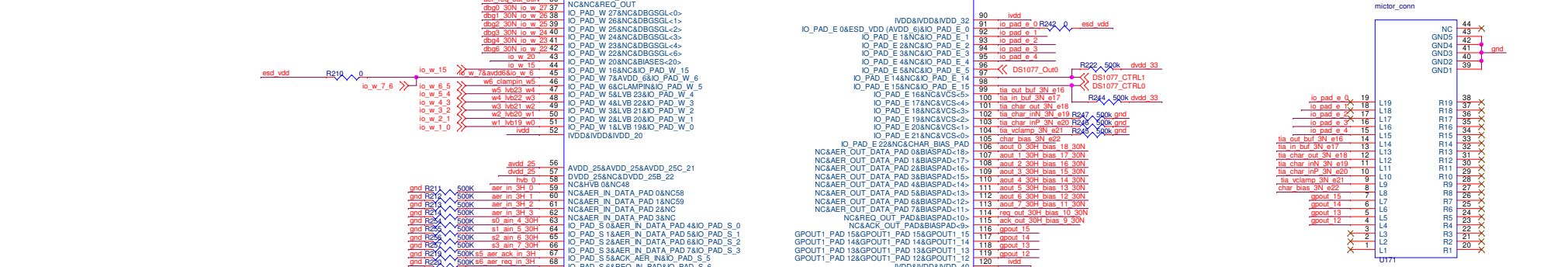
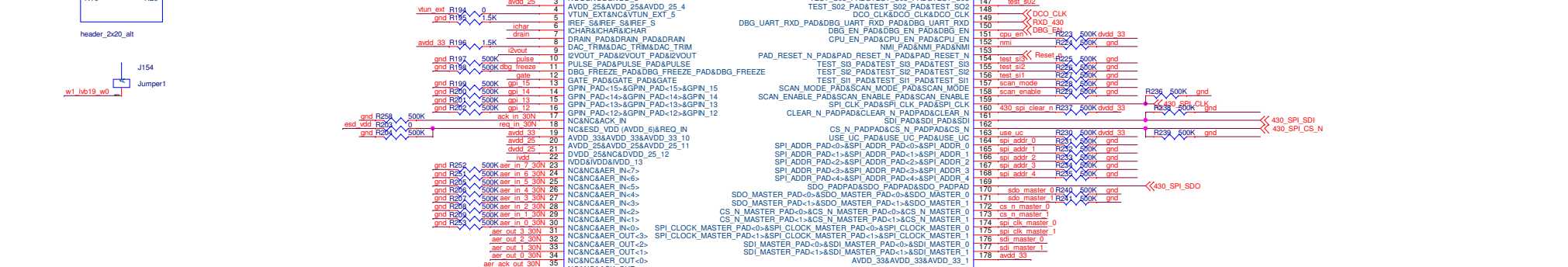
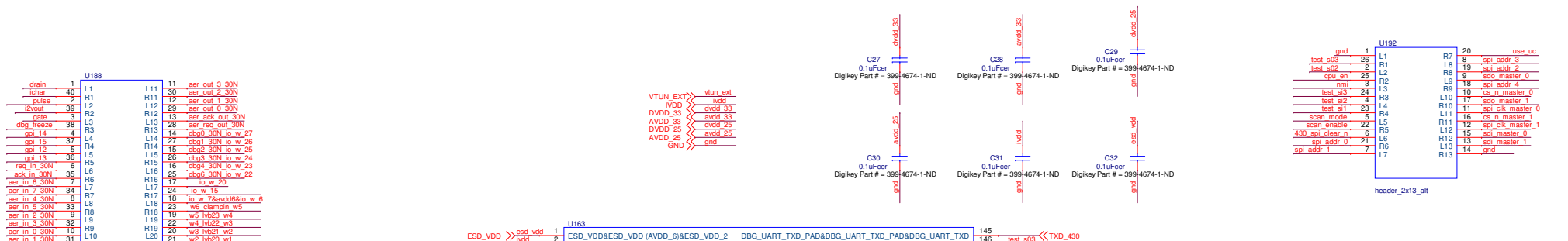
Vertical and Horizontal synchronization pulse OUT

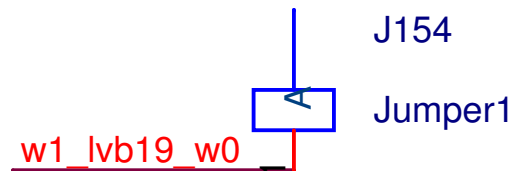
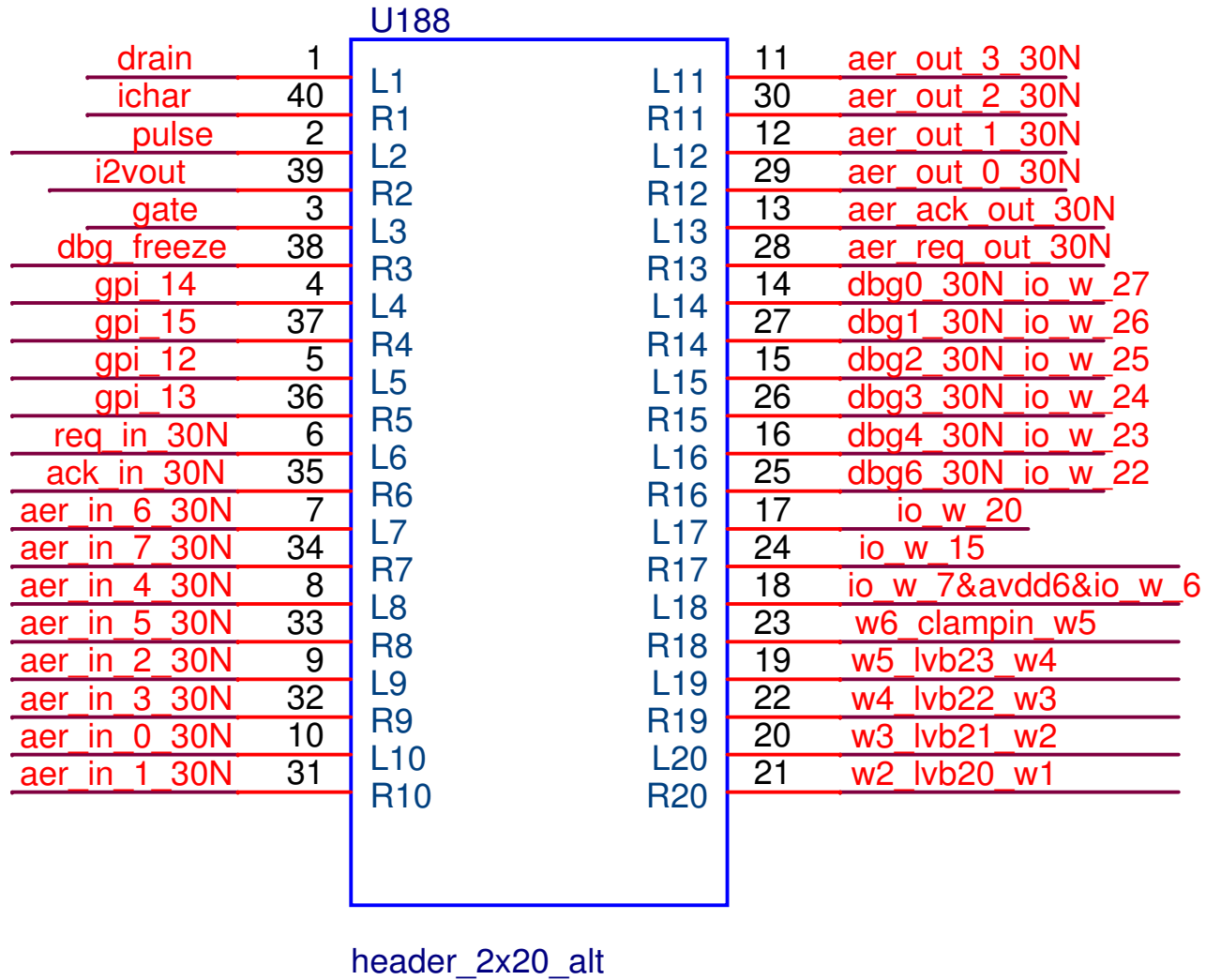
Clock for output data

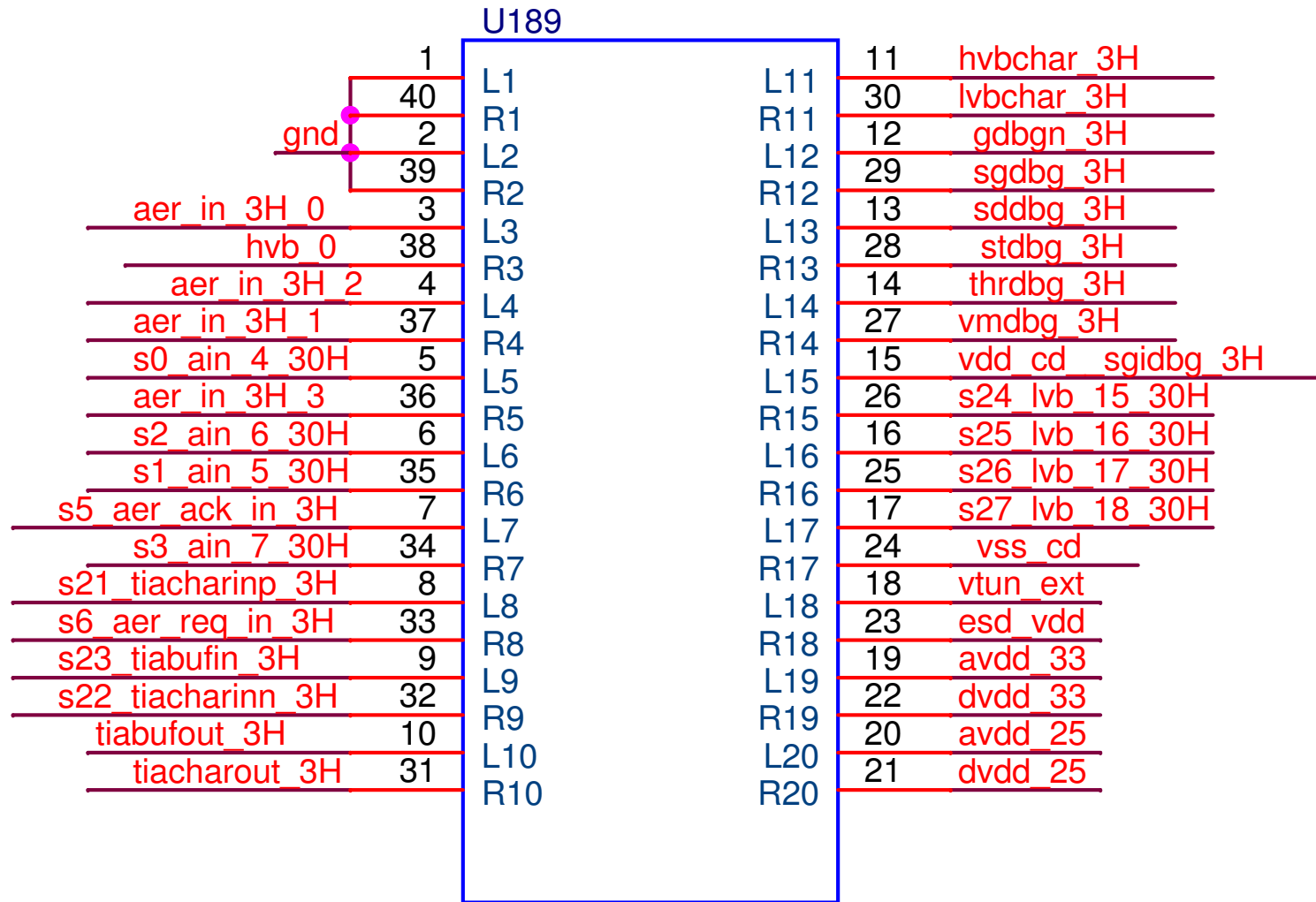


Connector

Digikey part # = S5573-ND

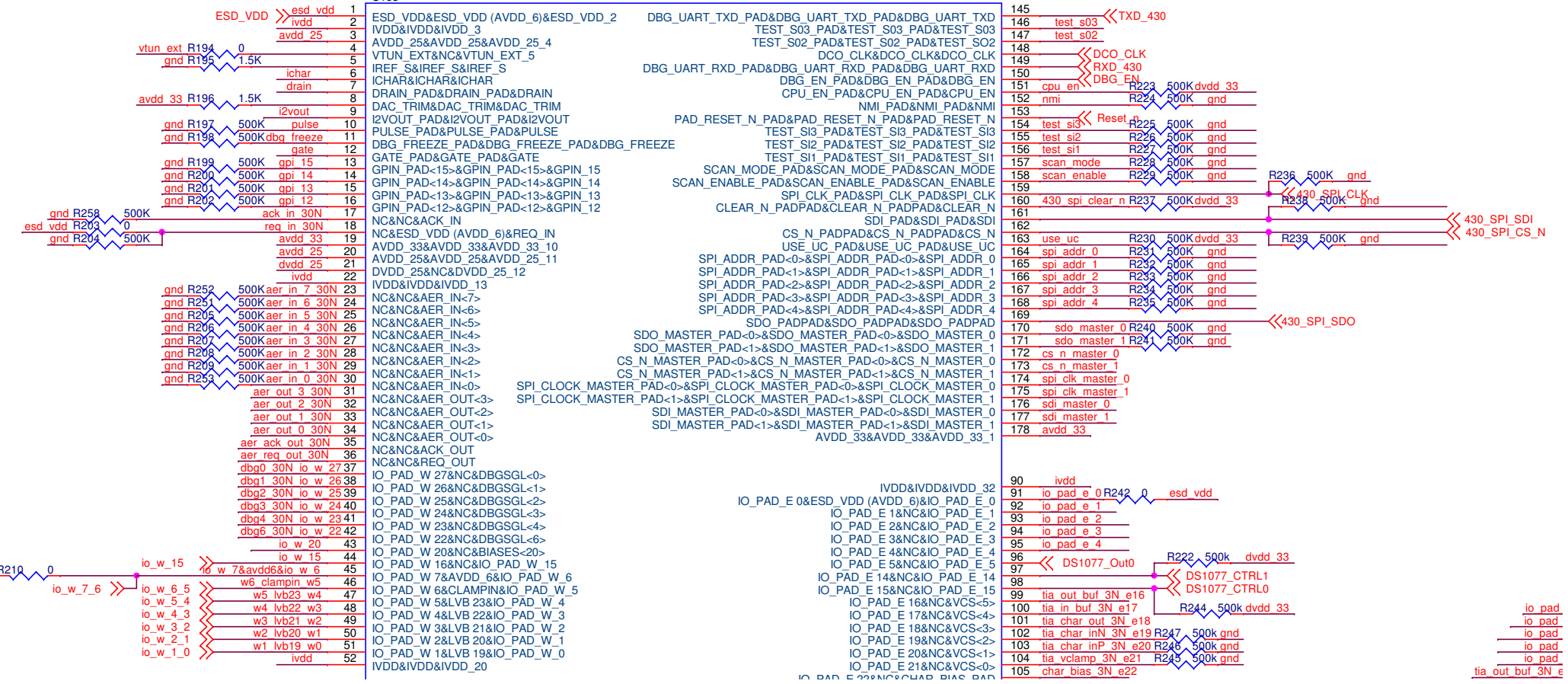


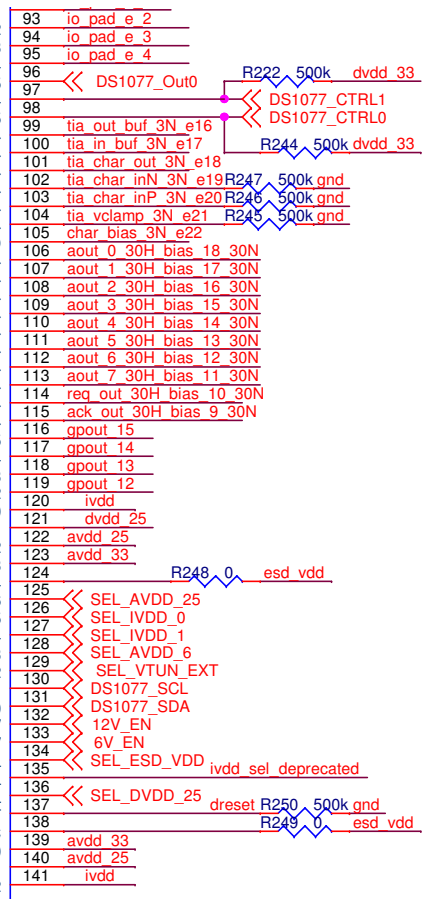
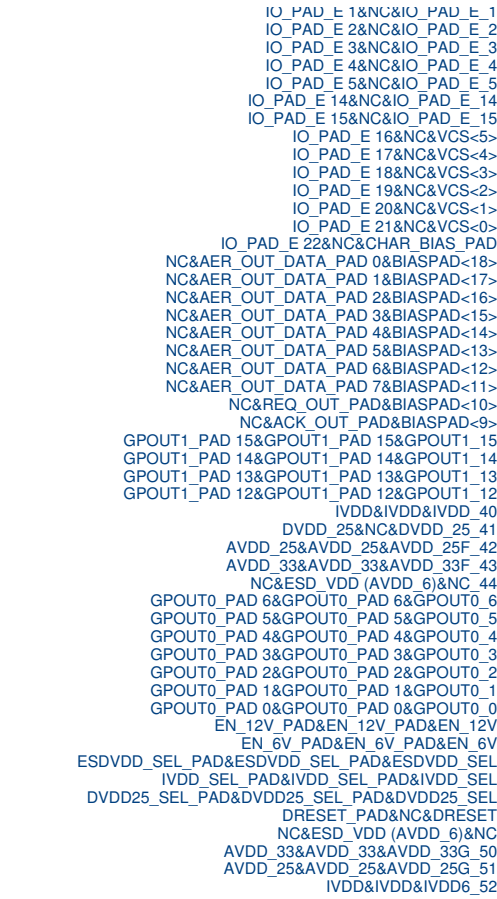
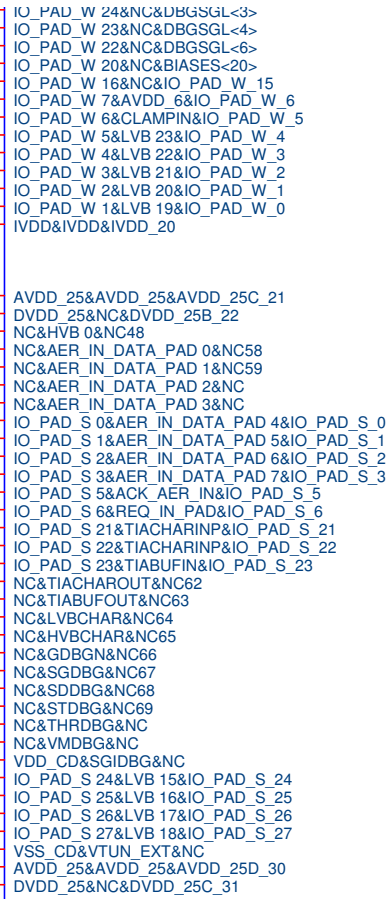
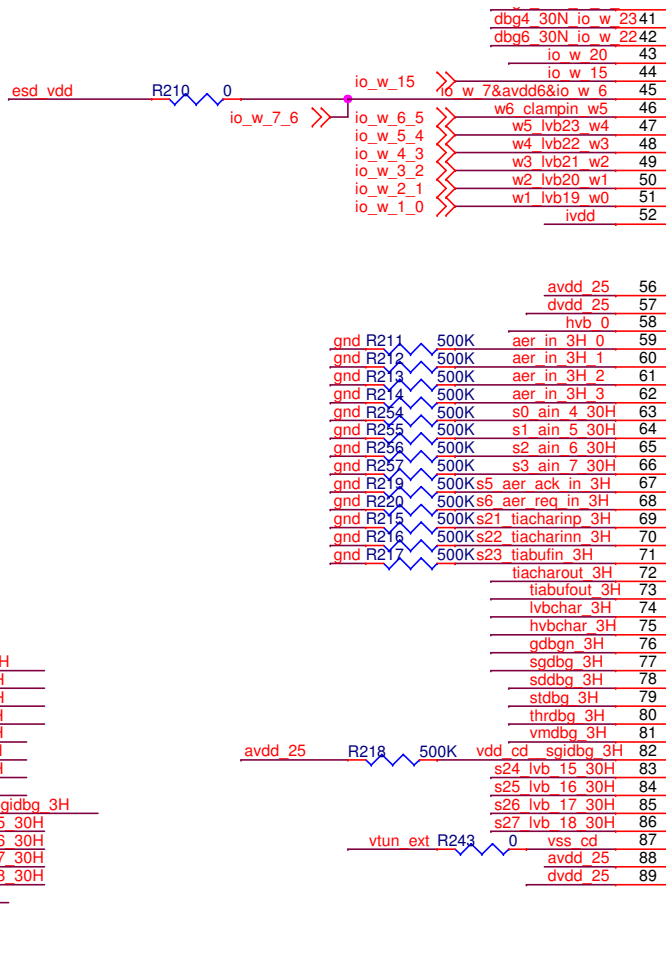




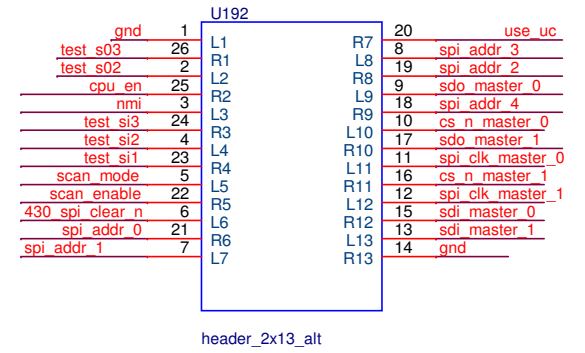
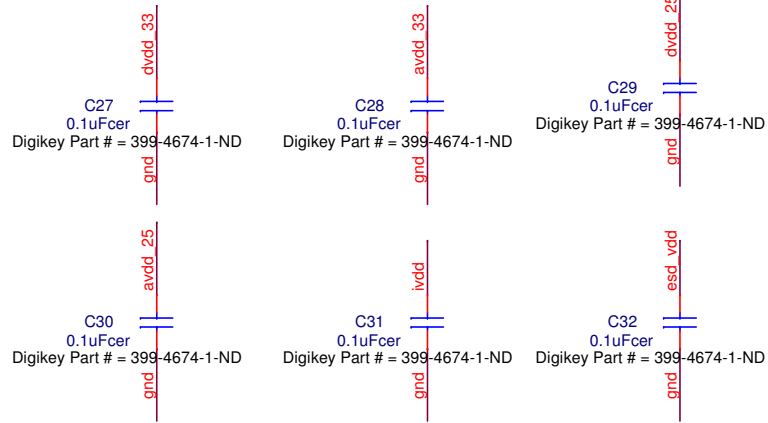
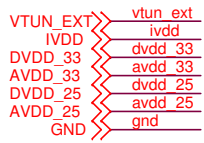
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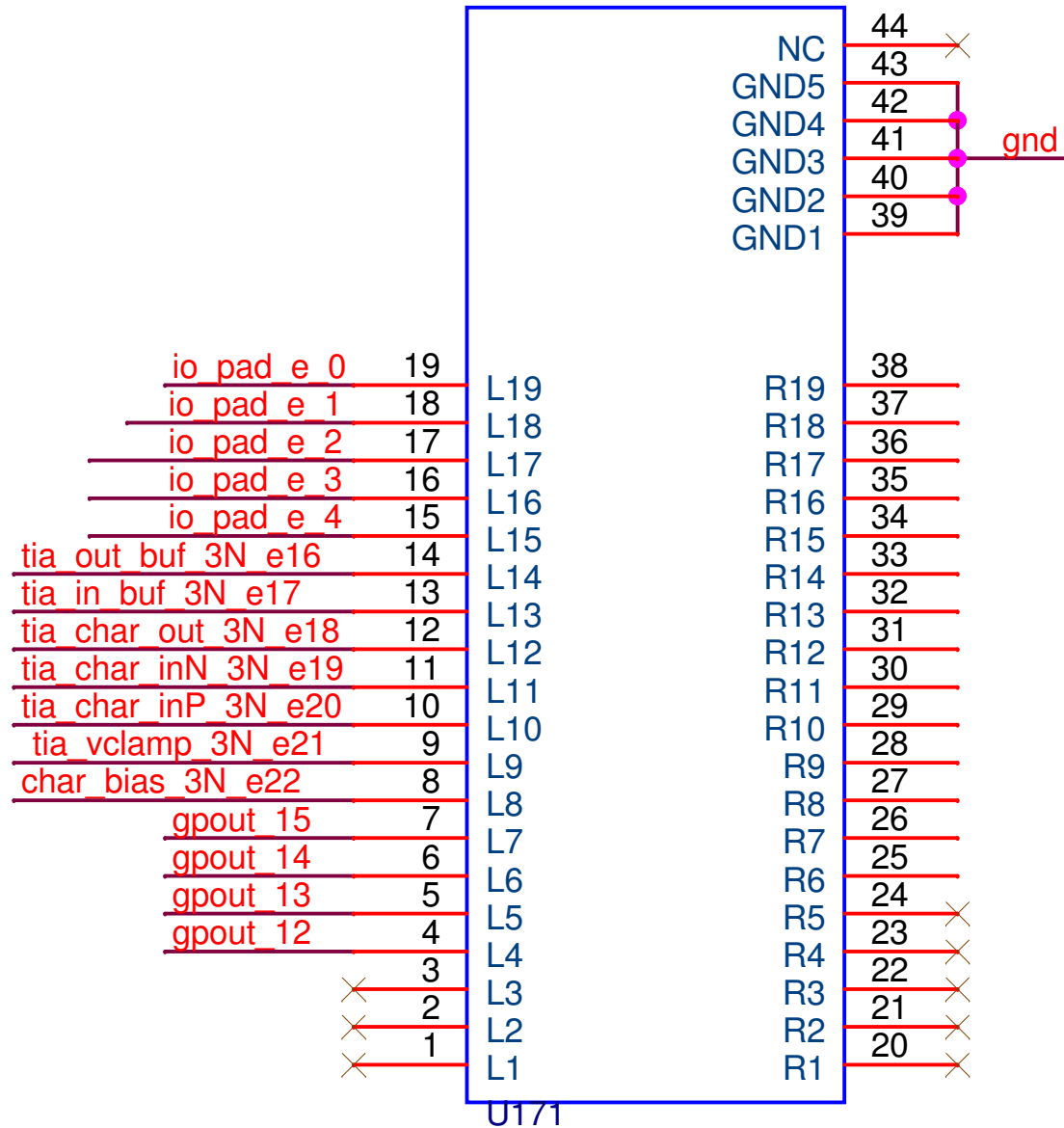


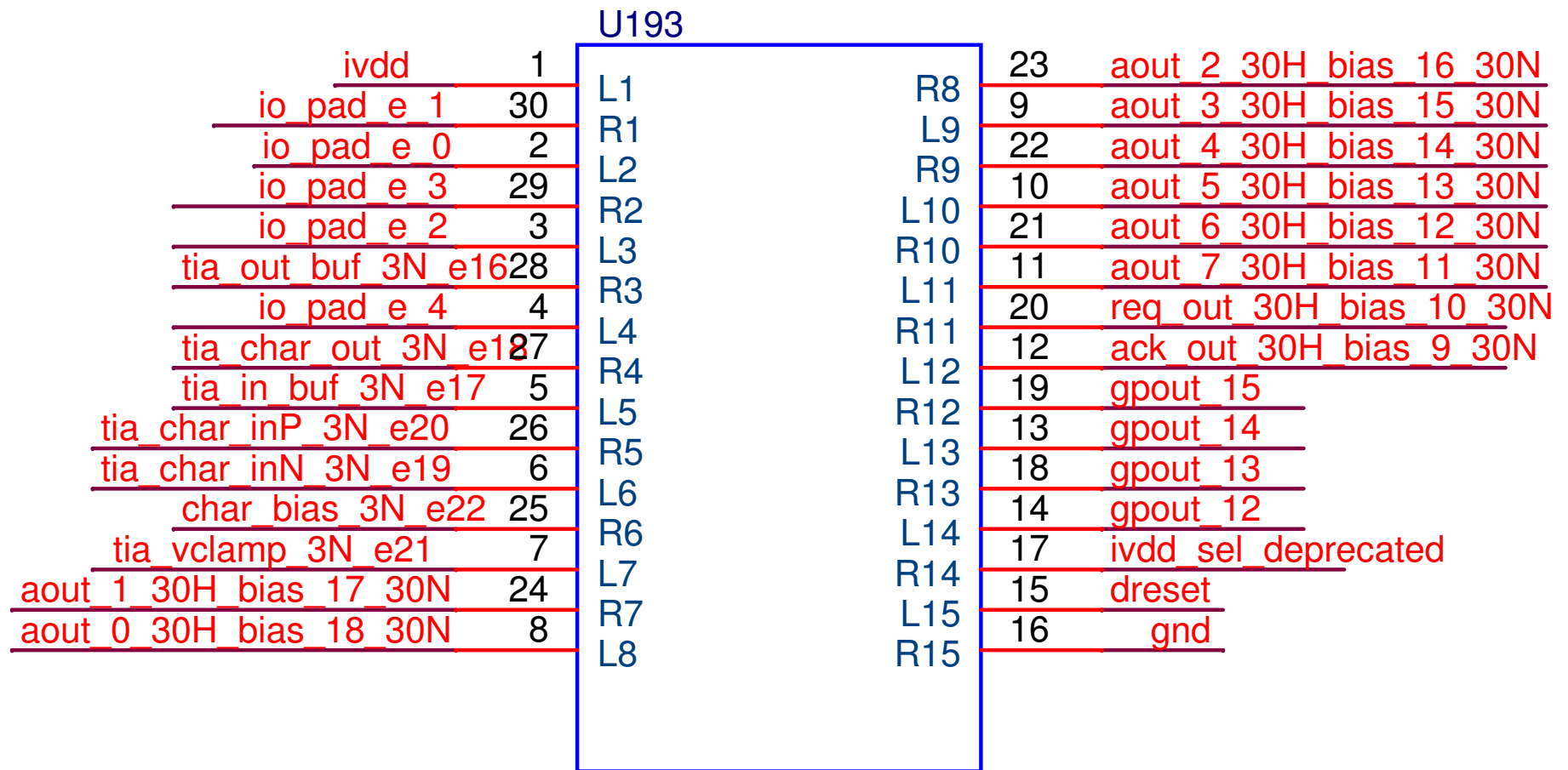


rasp30_2Rings_renamedPins



mictor_conn





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