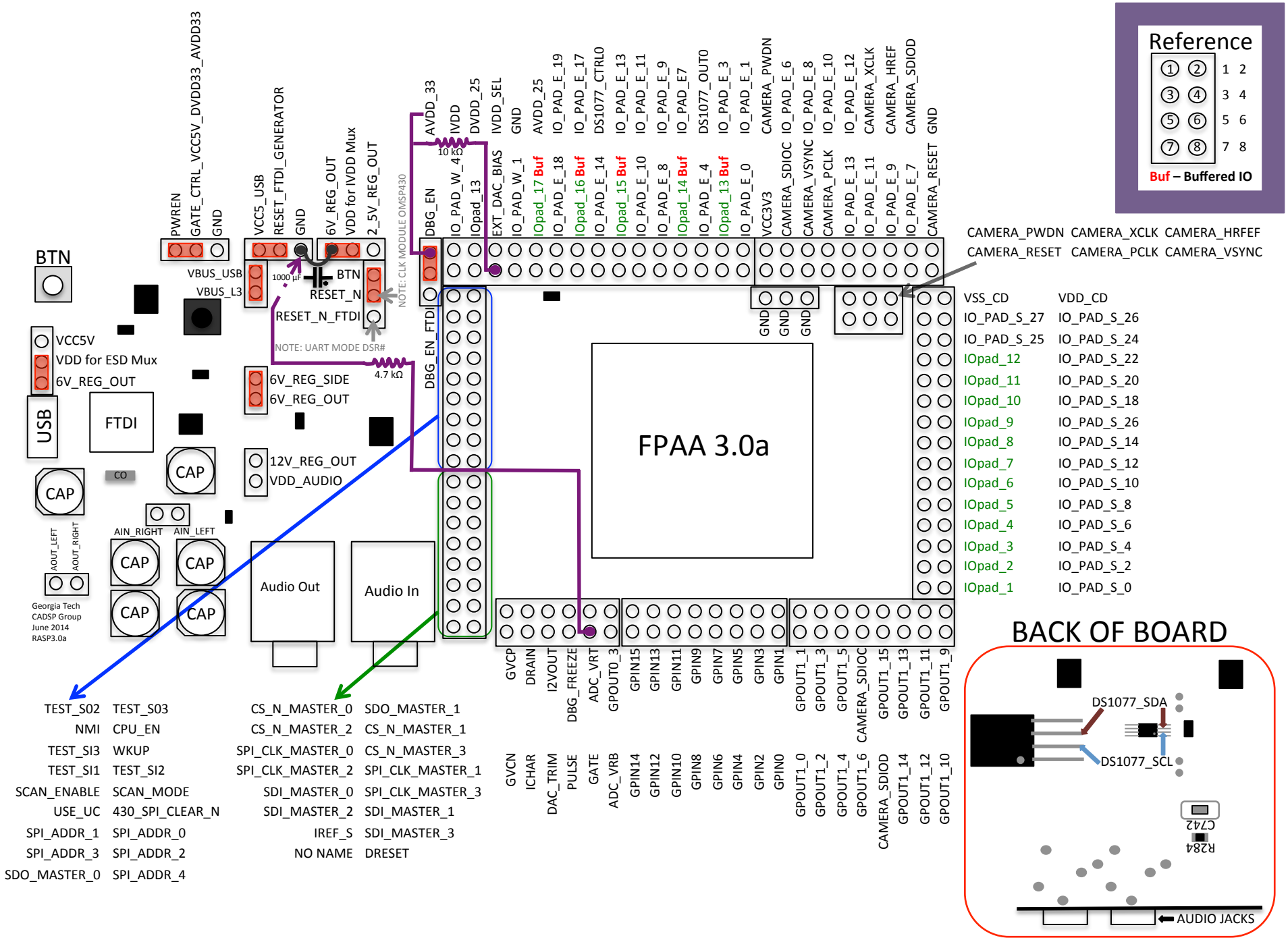
Reference
1 2
3 4
5 6
7 8
Buf – Buffered IO
FPAA 3.0a
BTN
USB
FTDI
CAP
Audio Out
Audio In
VCC5V
VDD for ESD Mux
6V_REG_OUT
VBUS_USB
VBUS_L3
1000 µF
RESET_N
RESET_N_FTDI
NOTE: UART MODE DSR#
NOTE: CLK MODULE OMSP430
4.7 kΩ
10 kΩ
6V_REG_SIDE
12V_REG_OUT
VDD_AUDIO
AIN_RIGHT
AIN_LEFT
AOUT_LEFT
AOUT_RIGHT
Georgia Tech
CADSP Group
June 2014
RASP3.0a
PWREN
GATE_CTRL_VCC5V_DVDD33_AVDD33
GND
VCC5_USB
RESET_FTDI_GENERATOR
6V_REG_OUT
VDD for IVDD Mux
2_5V_REG_OUT
AVDD_33
IVDD
DVDD_25
IVDD_SEL
AVDD_25
DBG_EN
DBG_EN_FTDI
IO_PAD_W_4
IOpad_13
EXT_DAC_BIAS
IO_PAD_W_1
IOpad_17 Buf
IOpad_16 Buf
IOpad_15 Buf
IOpad_14 Buf
IOpad_13 Buf
VCC3V3
CAMERA_PWDN
CAMERA_SDIOC
CAMERA_VSYNC
CAMERA_PCLK
CAMERA_XCLK
CAMERA_HREF
CAMERA_SDIOD
CAMERA_RESET
CAMERA_PWDN CAMERA_XCLK CAMERA_HRFEF
CAMERA_RESET CAMERA_PCLK CAMERA_VSYNC
VSS_CD VDD_CD
IO_PAD_S_27 IO_PAD_S_26
IO_PAD_S_25 IO_PAD_S_24
IOpad_12 IO_PAD_S_22
IOpad_11 IO_PAD_S_20
IOpad_10 IO_PAD_S_18
IOpad_9 IO_PAD_S_26
IOpad_8 IO_PAD_S_14
IOpad_7 IO_PAD_S_12
IOpad_6 IO_PAD_S_10
IOpad_5 IO_PAD_S_8
IOpad_4 IO_PAD_S_6
IOpad_3 IO_PAD_S_4
IOpad_2 IO_PAD_S_2
IOpad_1 IO_PAD_S_0
GVCP
DRAIN
I2VOUT
DBG_FREEZE
ADC_VRT
GPOUT0_3
GPIN15
GPIN13
GPIN11
GPIN9
GPIN7
GPIN5
GPIN3
GPIN1
GPOUT1_1
GPOUT1_3
GPOUT1_5
CAMERA_SDIOC
GPOUT1_15
GPOUT1_13
GPOUT1_11
GPOUT1_9
GVCN
ICHAR
DAC_TRIM
PULSE
GATE
ADC_VRB
GPIN14
GPIN12
GPIN10
GPIN8
GPIN6
GPIN4
GPIN2
GPIN0
GPOUT1_0
GPOUT1_2
GPOUT1_4
GPOUT1_6
CAMERA_SDIOD
GPOUT1_14
GPOUT1_12
GPOUT1_10
TEST_S02 TEST_S03
NMI CPU_EN
TEST_SI3 WKUP
TEST_SI1 TEST_SI2
SCAN_ENABLE SCAN_MODE
USE_UC 430_SPI_CLEAR_N
SPI_ADDR_1 SPI_ADDR_0
SPI_ADDR_3 SPI_ADDR_2
SDO_MASTER_0 SPI_ADDR_4
CS_N_MASTER_0 SDO_MASTER_1
CS_N_MASTER_2 CS_N_MASTER_1
SPI_CLK_MASTER_0 CS_N_MASTER_3
SPI_CLK_MASTER_2 SPI_CLK_MASTER_1
SDI_MASTER_0 SPI_CLK_MASTER_3
SDI_MASTER_2 SDI_MASTER_1
IREF_S SDI_MASTER_3
NO NAME DRESET
BACK OF BOARD
DS1077_SDA
DS1077_SCL
C742
R284
AUDIO JACKS