RASP30_IC_Board (Rev1)

Allegro Schematic:
   rasp30_ic_board.opj

Allegro PCB:
   rasp30_ic_board.brd

17 April 2013
Choose voltage supply with a 0 Ohm resistor

SCL = clock for I2C-bus command
SDA = Data for I2C-bus command

Clock for external INPUT

SCL & SDA for I2C

Vertical and Horizontal synchronization pulse OUT

Clock for output data